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Portmann, C.; Chu, A.; Hays, N.; Sidiropoulos, S.; Stark, D.; Chau, P.; Donn Garlepp, B.

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IEEE Micro , Volume: 18 Issue: 1 , Jan.-Feb. 1998

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9 A semidigital dual delay-locked loop

Sidiropoulos, S.; Horowitz, M.A.

Solid-State Circuits, IEEE Journal of , Volume: 32 Issue: 11 , Nov. 1997

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10 A semi-digital DLL with unlimited phase shift capability and 0.08-4 operating range

Sidiropoulos, S.; Horowitz, M.

Solid-State Circuits Conference, 1997. Digest of Technical Papers. 43rd ISSCC
IEEE International , 1997

Page(s): 332 -333, 481

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Solid-State Circuits, IEEE Journal of , Volume: 31 Issue: 1 , Jan. 1996

Page(s): 38 -45

[\[Abstract\]](#) [\[PDF Full-Text \(660 KB\)\]](#) **JNL****12 A 700 Mbps/pin CMOS signalling interface using current integratin receivers***Sidiropoulos, S.; Horowitz, M.*

VLSI Circuits, 1996. Digest of Technical Papers., 1996 Symposium on , 1996

Page(s): 142 -143

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Custom Integrated Circuits Conference, 1995., Proceedings of the IEEE 1995

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